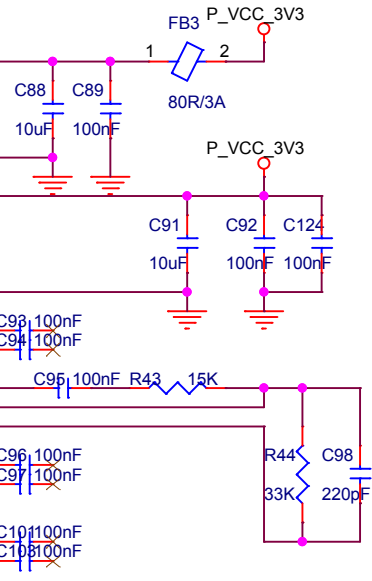
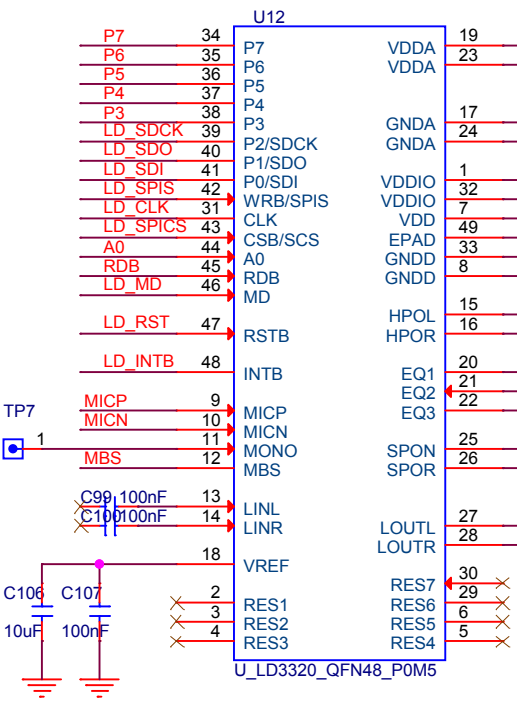
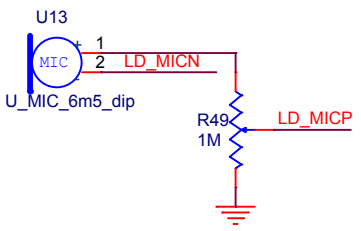
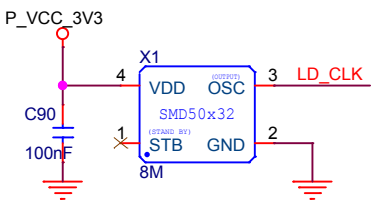
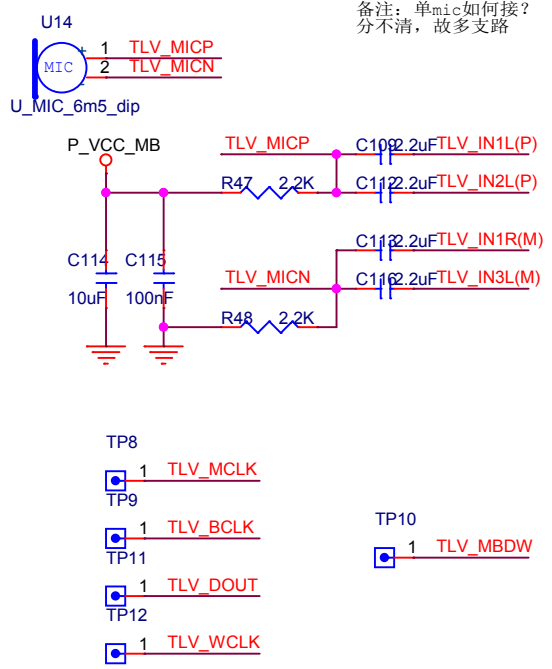
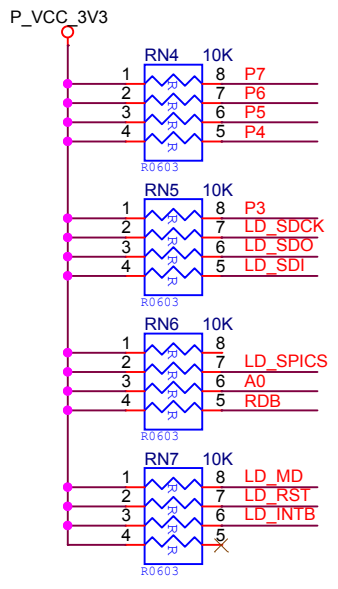
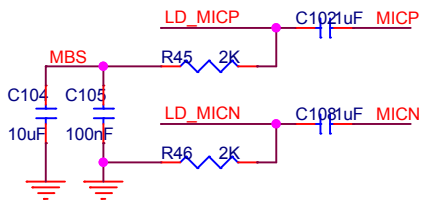


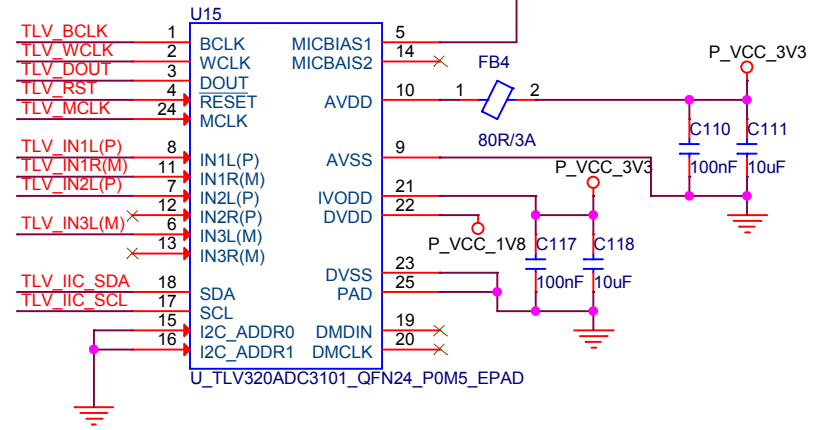
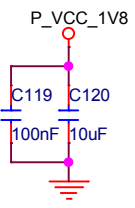
LD\_SDCK >> LD\_SDCK  
 LD\_SDI >> LD\_SDI  
 LD\_SPIS >> LD\_SPIS  
 LD\_SPICS >> LD\_SPICS  
 LD\_RST >> LD\_RST  
 LD\_INTB >> LD\_INTB  
 TLV\_IIC\_SDA >> TLV\_IIC\_SDA  
 TLV\_IIC\_SCL >> TLV\_IIC\_SCL  
 TLV\_RST >> TLV\_RST

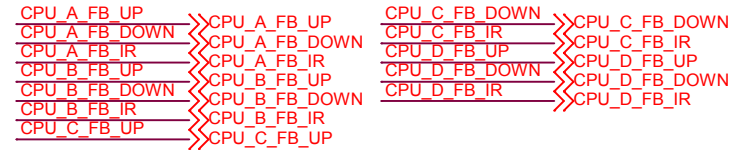
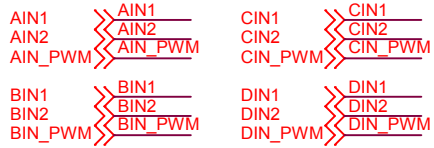


LD\_SDO >> LD\_SDO  
 TLV\_MBDW >> TLV\_MBDW

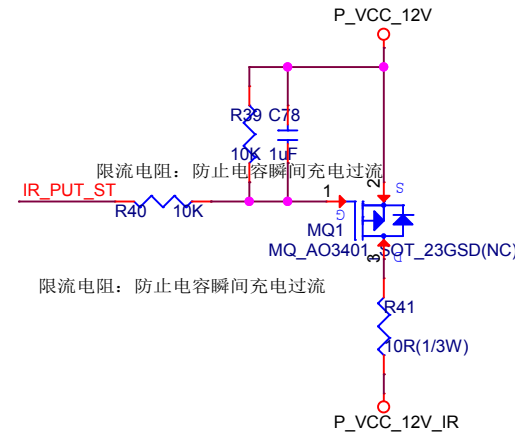
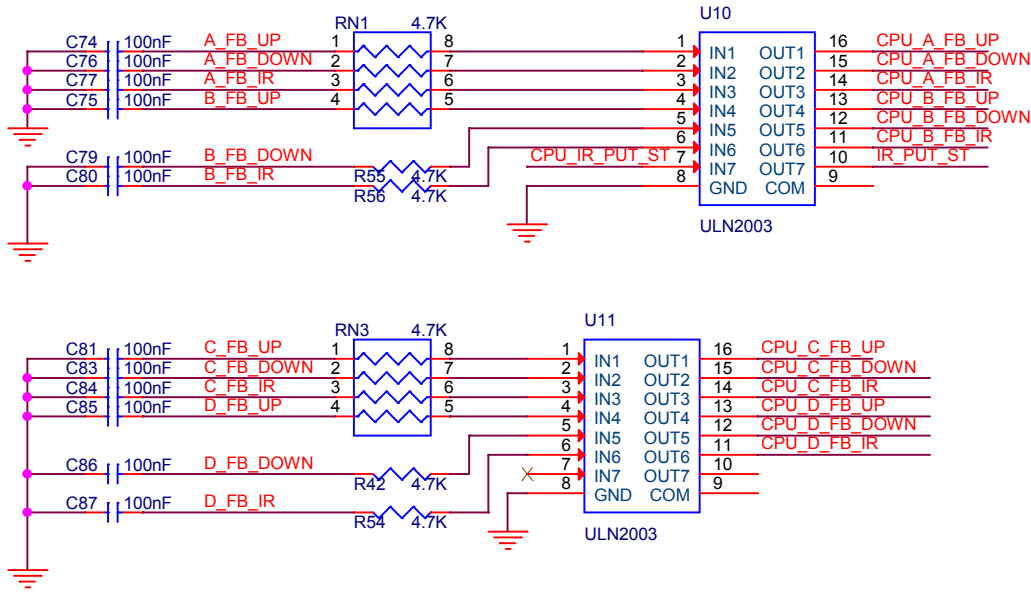
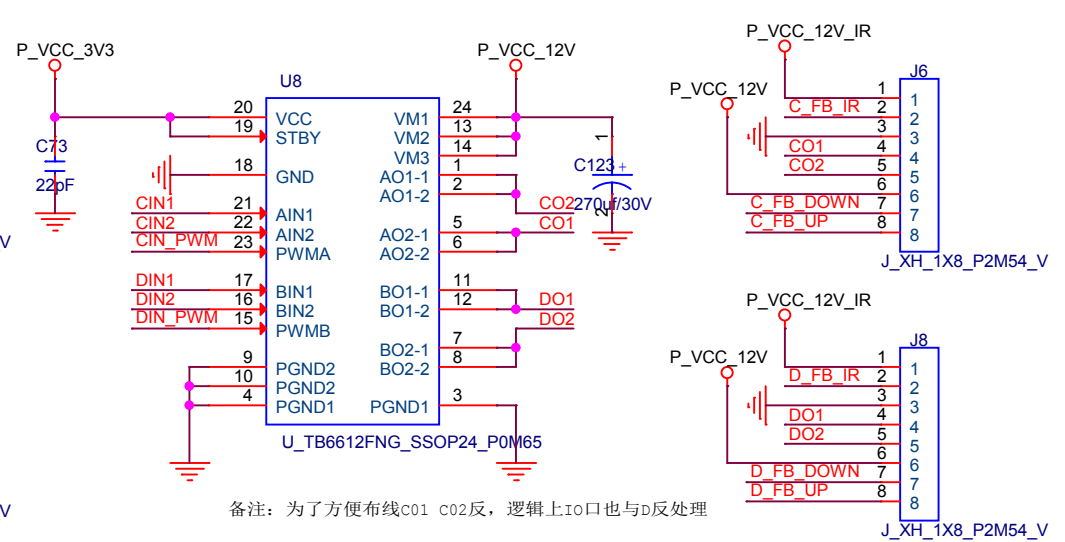
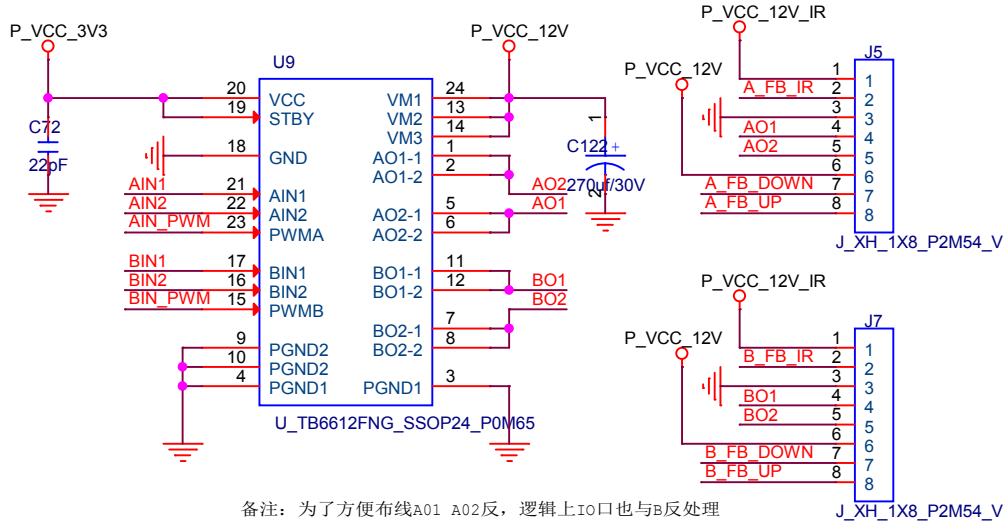


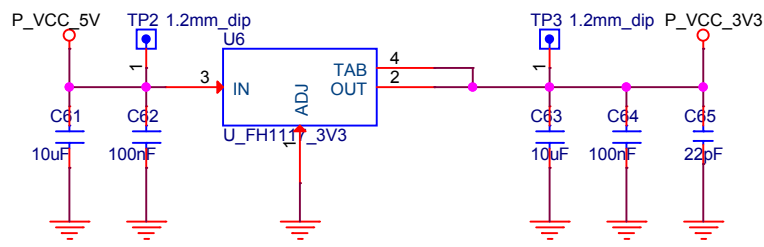
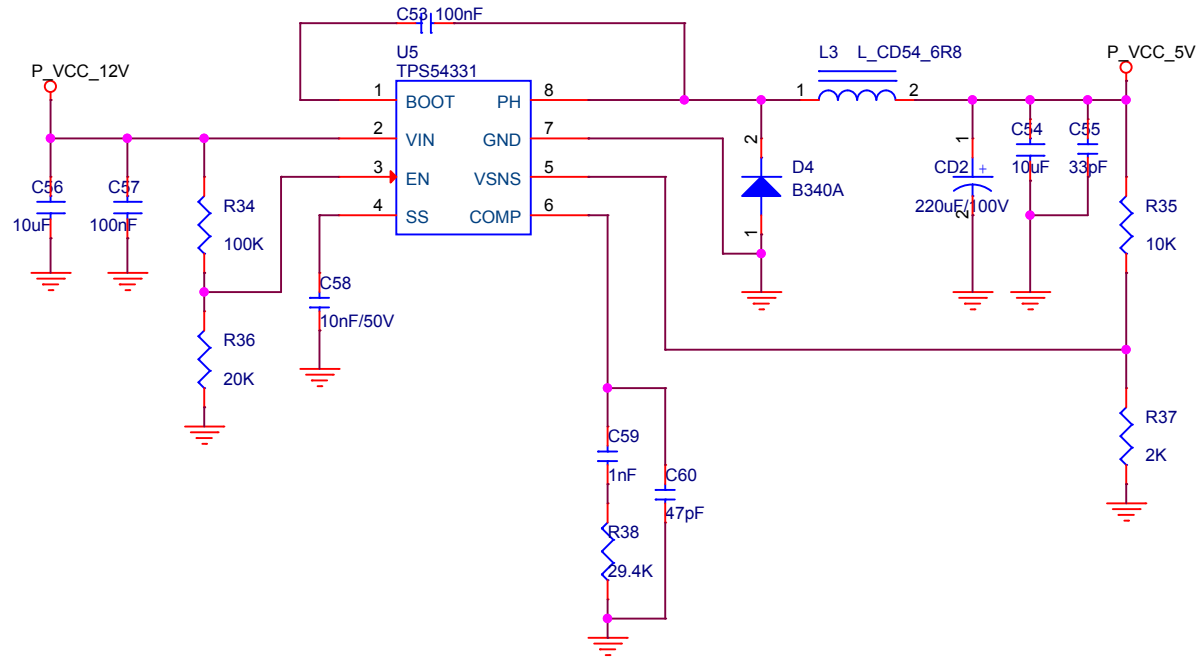
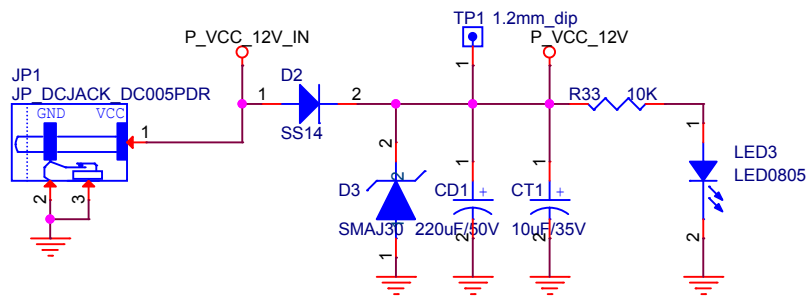
备注：单mic如何接？  
分不清，故多支路





CPU\_IR\_PUT\_ST >> CPU\_IR\_PUT\_ST

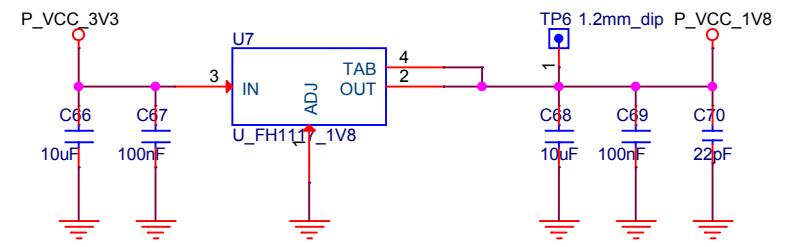
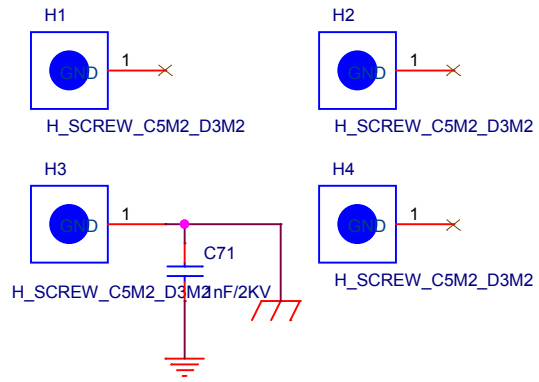




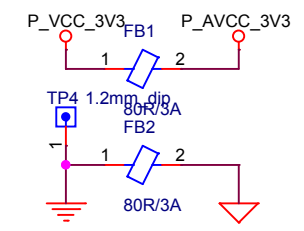
5V转3V3

12V转5V

备注：反馈电压是0.8v，通过反馈可得输出电压设计为4.8v

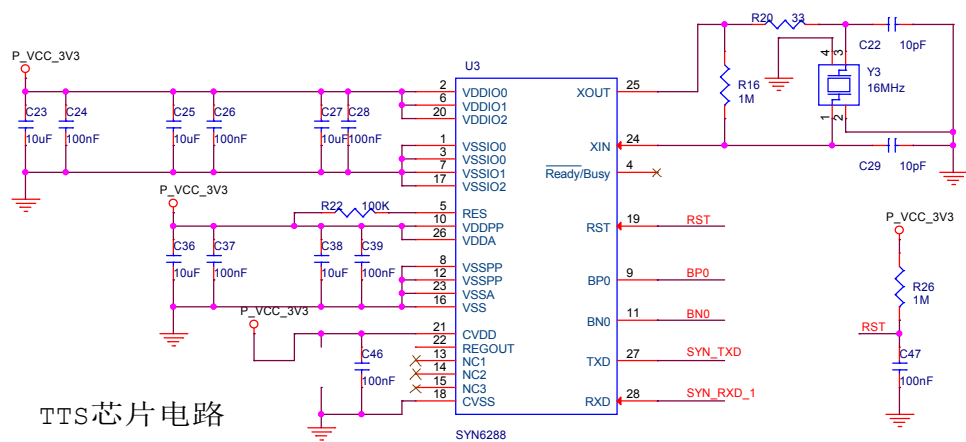


5V转1V8

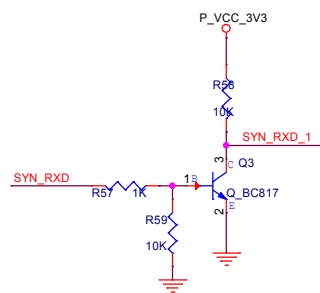


备注：原R53R50，现FB1和2和3可以考虑使用磁珠，具体型号待测

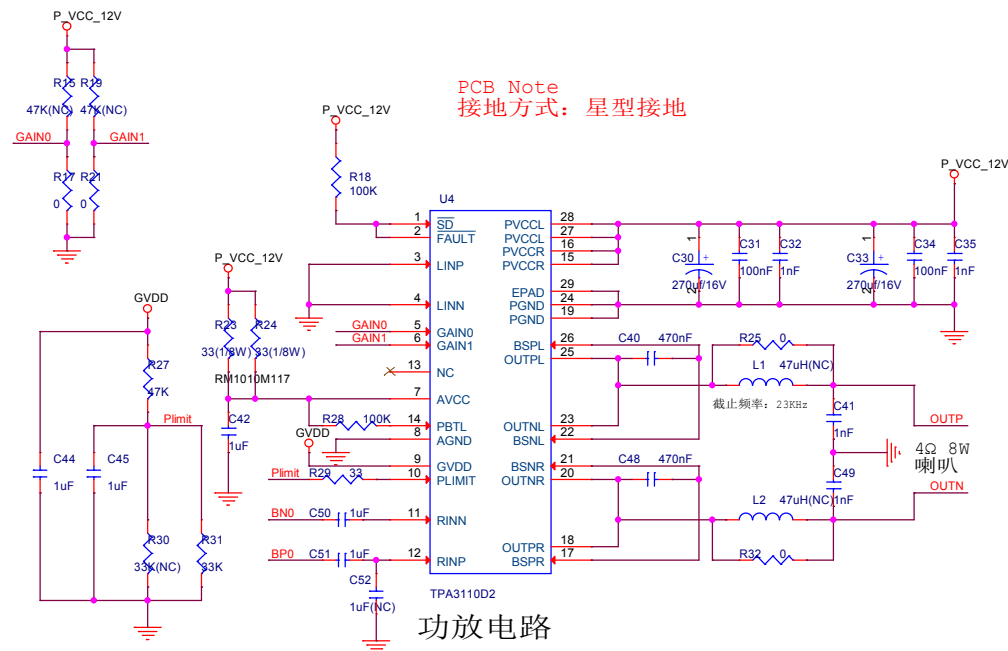
SYN\_RXD >> SYN\_RXD



TTS芯片电路



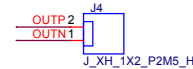
SYN\_TXD >> SYN\_TXD



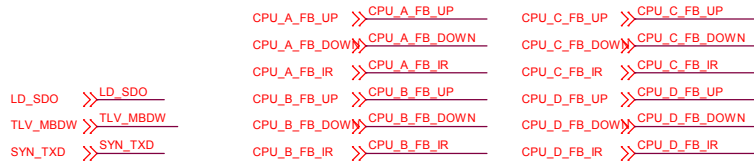
PCB Note  
接地方式：星型接地

功放电路

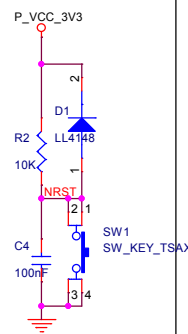
GVDD 输出7V (端子不连接, 默认为10W)  
 当只连接33k, Plimit设置为2.87V, 功率限制为10W  
 当通过端子端接5W\_1和5W\_2时, 33k和13k并联结  
 果为16.5k, 分压后, Plimit设置为1.88V, 功率限制为5W (设置不精确是为了精简预料)



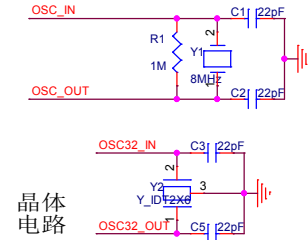
### 信号输入



### 复位电路



### 晶体电路



### 信号输出

